

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
21 May 2004 (21.05.2004)

PCT

(10) International Publication Number  
**WO 2004/042543 A1**

(51) International Patent Classification<sup>7</sup>: **G06F 1/32**

(21) International Application Number:  
PCT/IB2003/004695

(22) International Filing Date: 23 October 2003 (23.10.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
102 51 806.8 7 November 2002 (07.11.2002) DE

(71) Applicant (for DE only): **PHILIPS INTELLECTUAL  
PROPERTY & STANDARDS GMBH** [DE/DE]; Stein-  
damm 94, 20099 Hamburg (DE).

(71) Applicant (for AE, AG, AL, AM, AT, AU, AZ, BA, BB, BE,  
BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CY, CZ, DK,  
DM, DZ, EC, EE, EG, ES, FI, FR, GB, GD, GE, GH, GM,  
GR, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,  
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX,  
MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD,  
SE, SG, SK, SL, SY, SZ, TJ, TM, TN, TR, TT, TZ, UA, UG,

UZ, VC, VN, YU, ZA, ZM, ZW only): **KONINKLIJKE  
PHILIPS ELECTRONICS N.V.** [NL/NL]; Groenewoud-  
seweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **DEPPE, Carsten**  
[DE/DE]; c/o Philips Intellectual Property & Standards  
GmbH, Weissshausstr. 2, 52066 Aachen (DE). **WENDT,  
Matthias** [DE/DE]; c/o Philips Intellectual Property &  
Standards GmbH, Weissshausstr. 2, 52066 Aachen (DE).

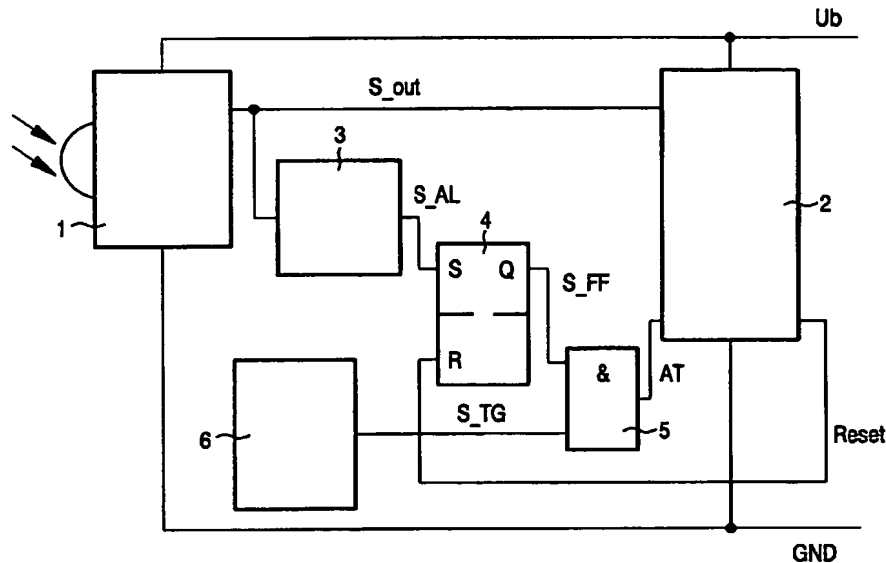
(74) Agent: **VOLMER, Georg**; Philips Intellectual Property &  
Standards GmbH, Weissshausstr. 2, 52066 Aachen (DE).

(81) Designated States (national): AE, AG, AL, AM, AT, AU,  
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,  
CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE,  
GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR,  
KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK,  
MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT,  
RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR,  
TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM,  
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),

[Continued on next page]

(54) Title: **CIRCUIT ARRANGEMENT FOR A MICROCONTROLLER AND METHOD OF OPERATING A REMOTE CON-  
TROL RECEIVER**



(57) Abstract: A circuit arrangement is described for a microcontroller having an improved energy consumption which is achieved in that the microcontroller (2) is connected to its operating clock only during evaluation of a signal. A switching means (5) through-connecting the operating clock when a "logic 1" signal is present at the control input is arranged between the microcontroller (2) and the clock generator (6). This "logic 1" signal is generated by setting a flip-flop (4, 7) for example, when receiving a remote control signal. The program run of the microcontroller (2) is stopped at a defined breakpoint so that there is no transient time.